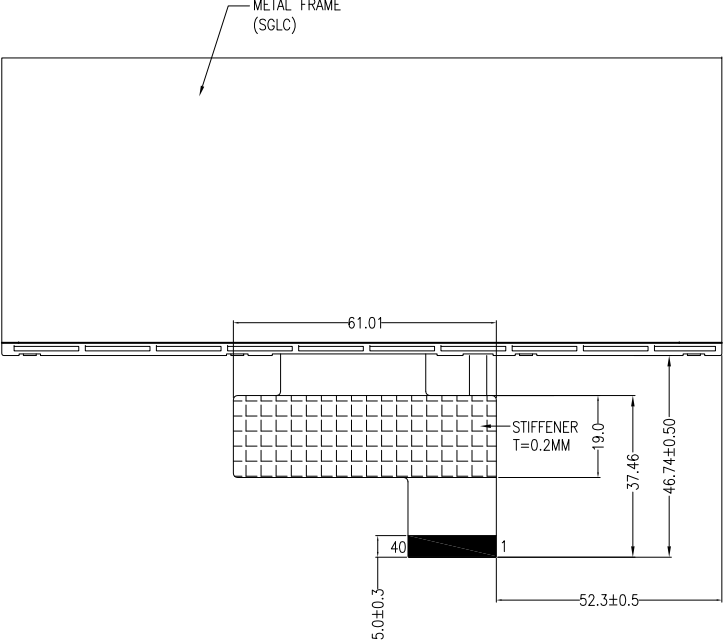
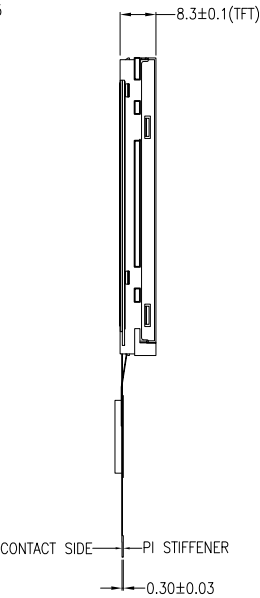
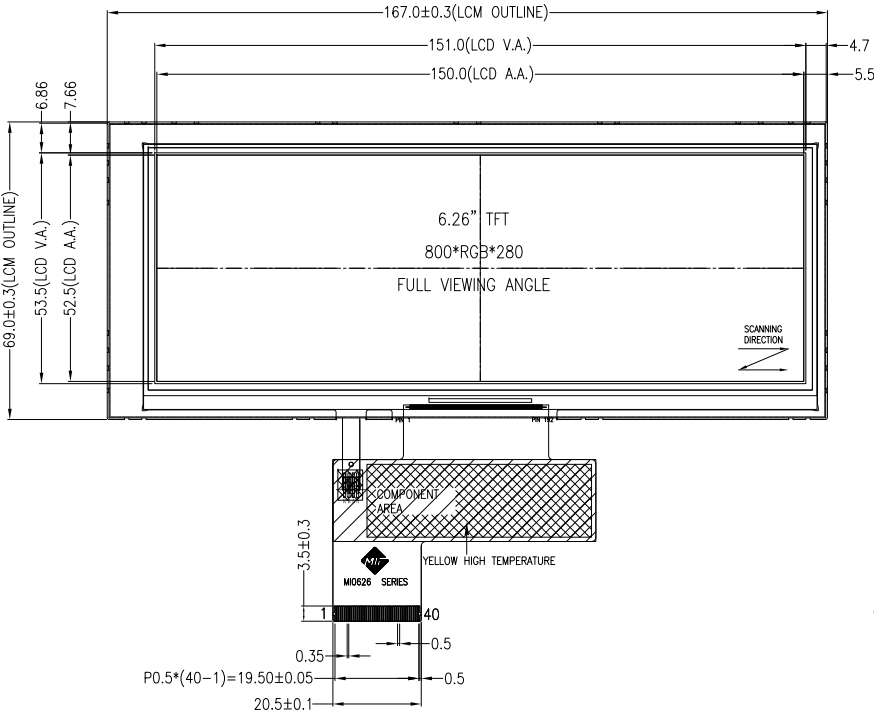


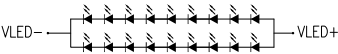
NO MECHANICAL HOUSING TO HOLD THE TFT AND THE BACKLIGHT UNIT ON THIS PRODUCT, SO THE END UNIT'S MECHANICAL DESIGN MUST INCLUDE A HOUSING FOR PREVENTING THE SEPARATION OF THE TFT AND THE BACKLIGHT UNIT.



PIN ASSIGNMENT FOR LCM	
PIN	SYMBOL
1	VLED-
2	VLED+
3	GND
4	VDD
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	DCLK
31	DISP
32	HSYNC
33	VSYNC
34	DE
35	NC(ATREN)
36	GND
37	NC(XRES)
38	NC(VDD_OTP)
39	NC(I2C_SCL)
40	NC(I2C_SDA)

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VDD=3.3V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: HX8272
5. OPERATING TEMP.: -20°C ~ 70°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. LED BACKLIGHT: WHITE LED, IF=40mA, VF=27V±1.8V
8. SURFACE LUMINANCE: 560 CD/M² (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
10. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
11. GENERAL TOLERANCE: ±0.3MM
12. ROHS COMPLIANT



BACKLIGHT CIRCUIT DIAGRAM

 MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	JACK	2023.12.21		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI0626AT-2
APPROVED BY:			UNIT: MM	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 1/1	TFT MODULE

01	FIRST ISSUE	JACK	2023.12.21
VER.	REVISED DESCRIPTION	REVISER	DATE